

Fig.1

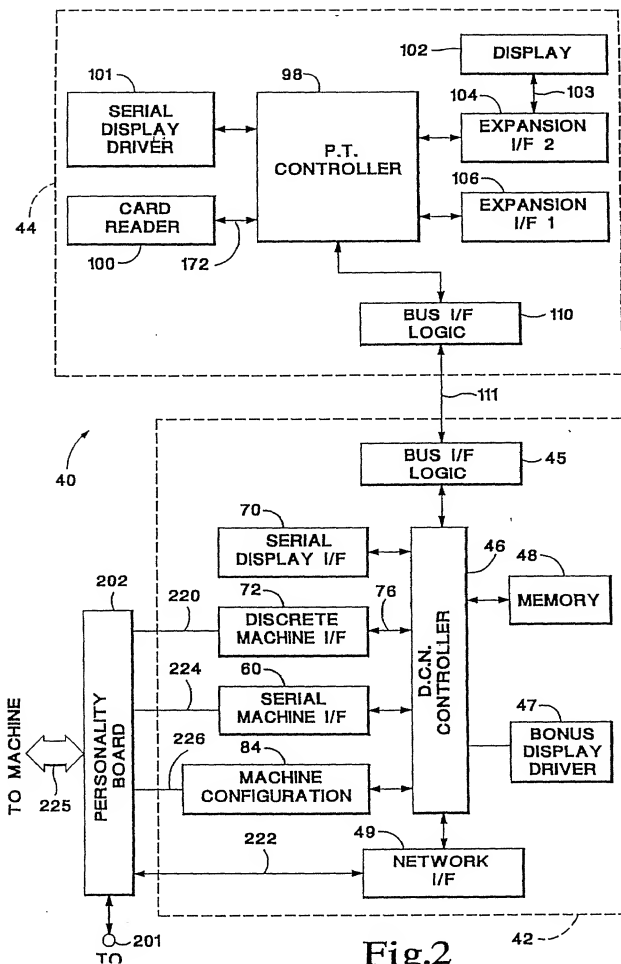


Fig.2

TO PERSONALITY BOARD 202
(FIG.2 AND FIG.9)

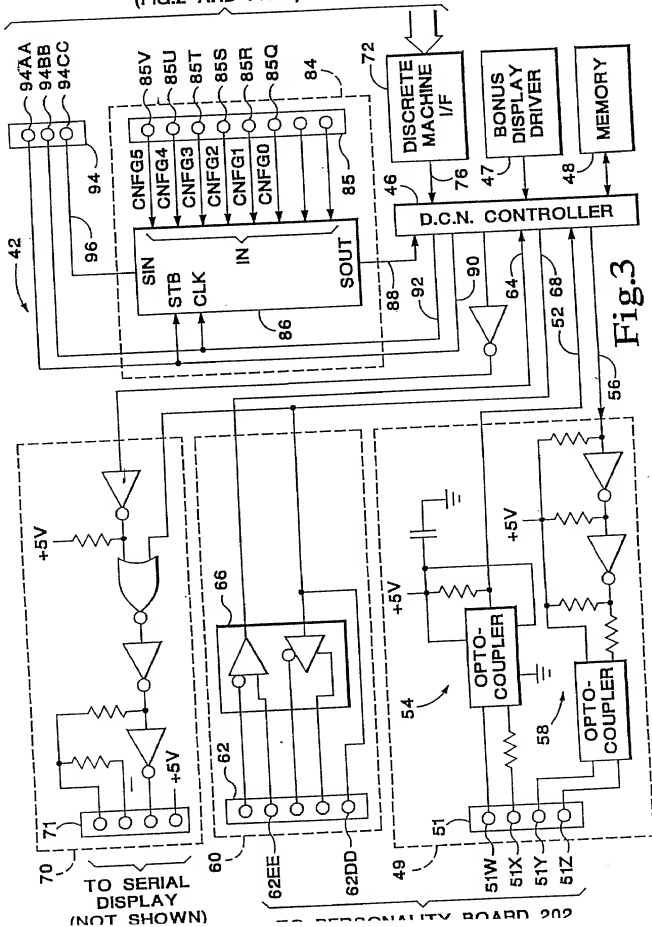
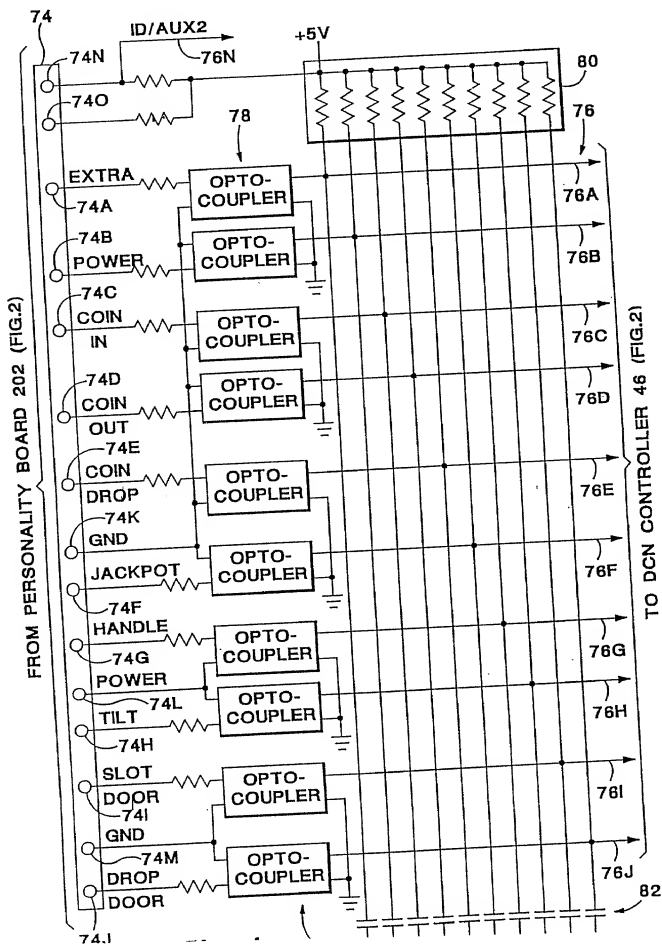


Fig.3

109040 02822360



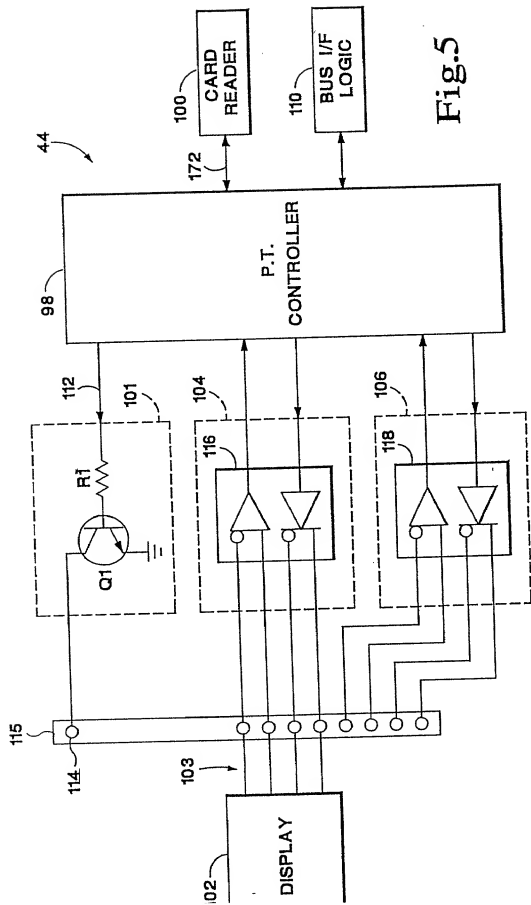
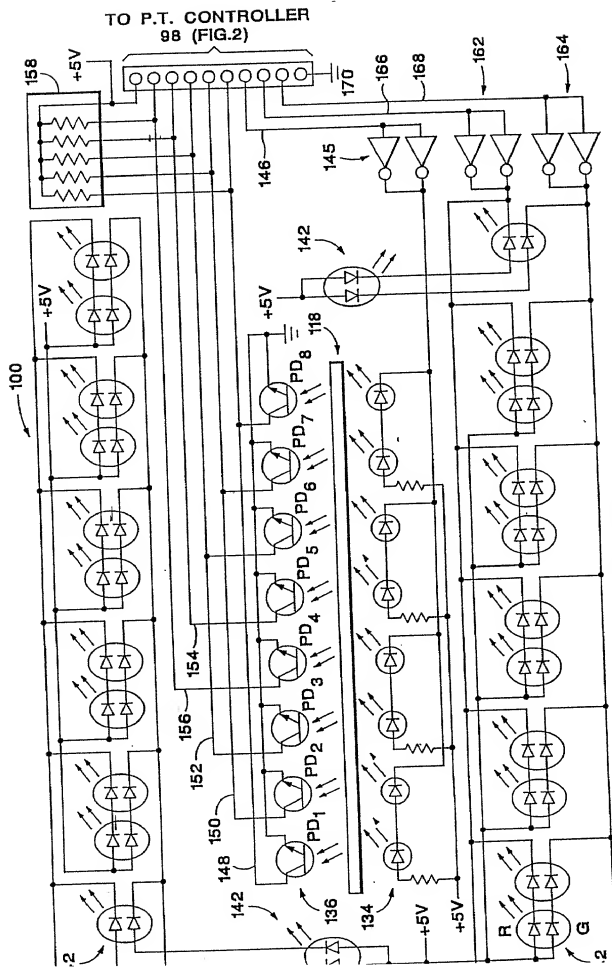
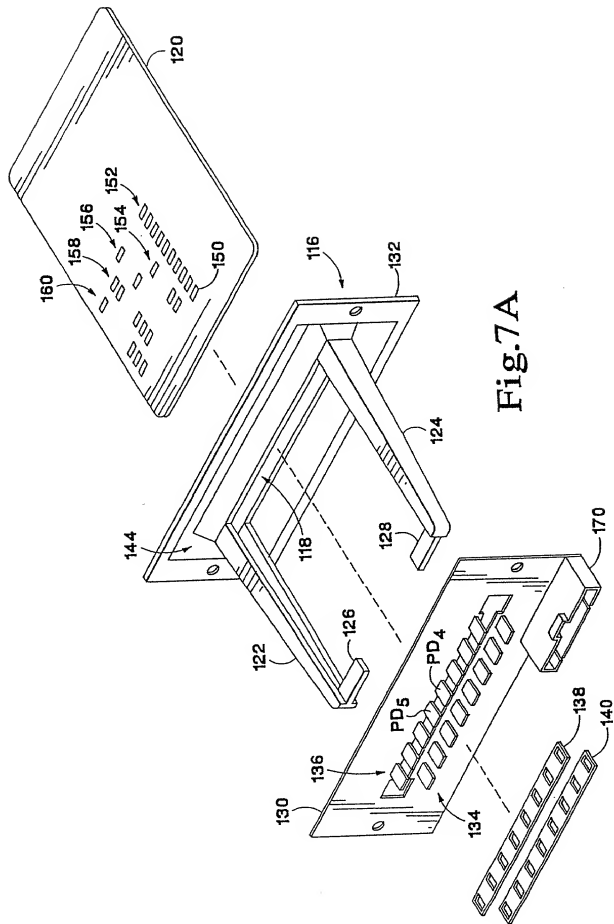


Fig.5





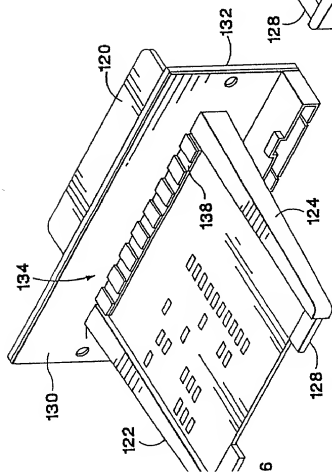


Fig. 7B

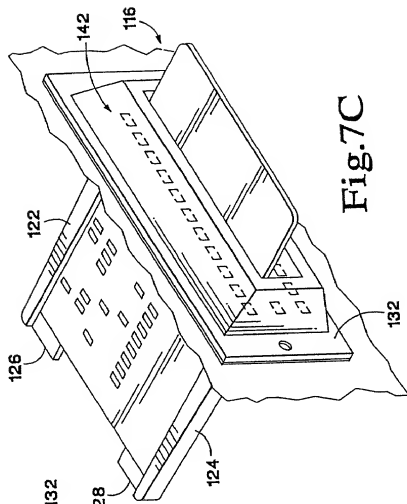


Fig. 7C

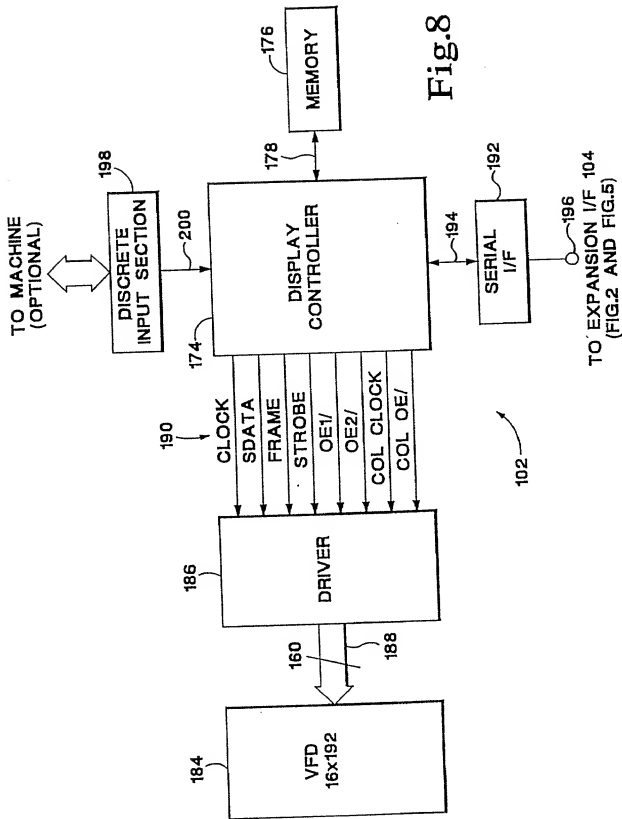
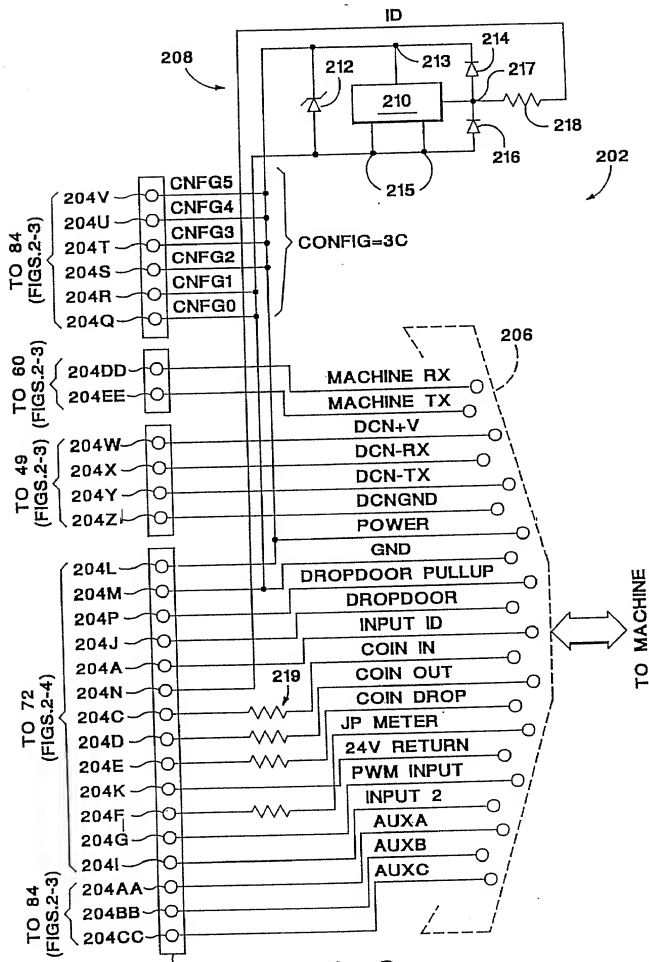
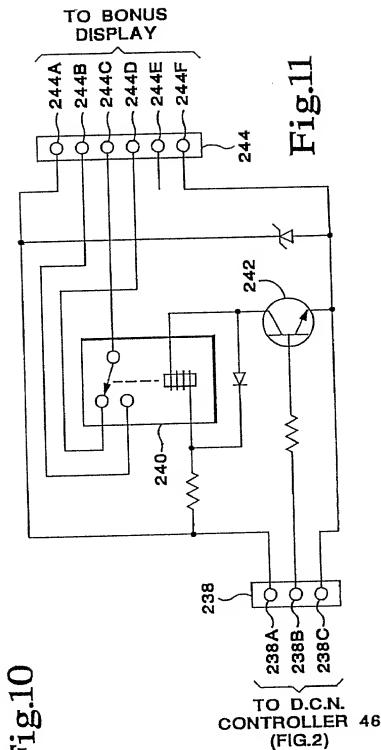
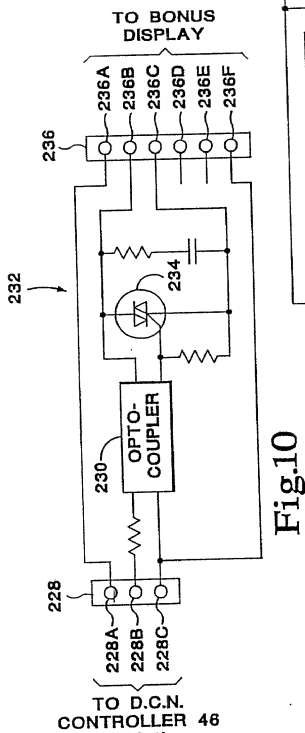


Fig.8





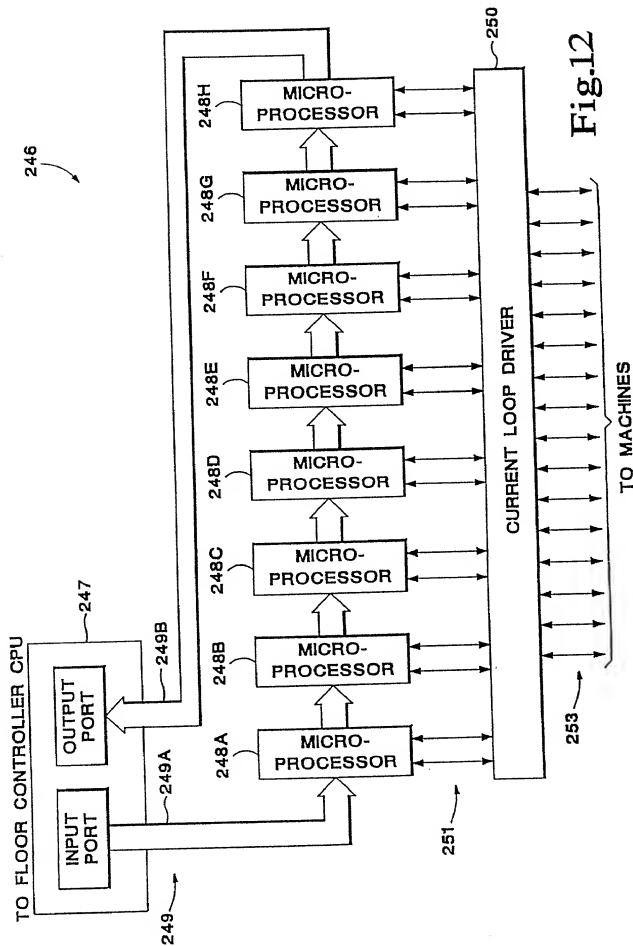


Fig.12

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graph TD; Start([POWER UP]) --> 254[VALIDATE RAM SETUP  
ALL HARDWARE]; 254 --> 255{RAM OK?}; 255 -- N --> 256([LOOP FOREVER]); 255 -- Y --> 258[READ UNIQUE ID FROM  
HARNESS]; 258 --> 260[PROCESS DISCRETE  
INPUTS]; 260 --> 262[PROCESS MACHINE  
SERIAL INTERFACE]; 262 --> 264[PROCESS NETWORK  
INTERFACE]; 264 --> 266[PROCESS PLAYER  
TRACKING INTERFACE]; 266 --> 260;
```

The flowchart illustrates the system's operational sequence. It begins with a 'POWER UP' event, leading to a process block 'VALIDATE RAM SETUP ALL HARDWARE' (254). A decision diamond 'RAM OK?' (255) follows. If the RAM is not OK (N), the system enters a 'LOOP FOREVER' state (256). If the RAM is OK (Y), the system proceeds to 'READ UNIQUE ID FROM HARNESS' (258), then 'PROCESS DISCRETE INPUTS' (260), 'PROCESS MACHINE SERIAL INTERFACE' (262), 'PROCESS NETWORK INTERFACE' (264), and 'PROCESS PLAYER TRACKING INTERFACE' (266). The flow then loops back to 'PROCESS DISCRETE INPUTS' (260).

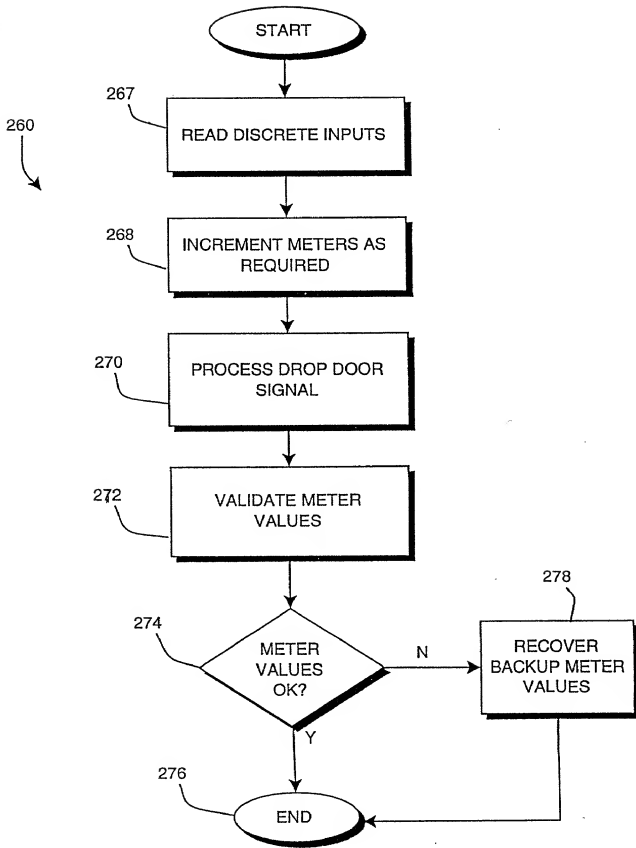


Fig.14

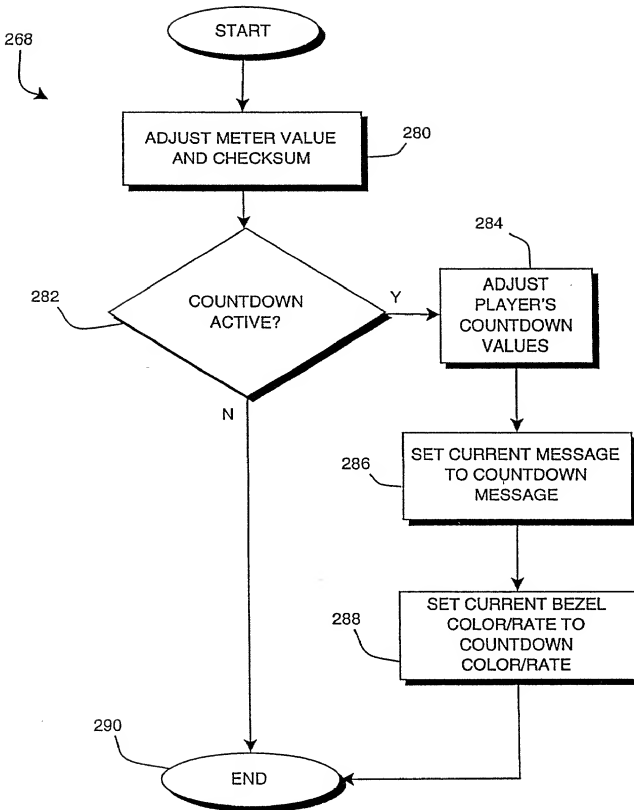
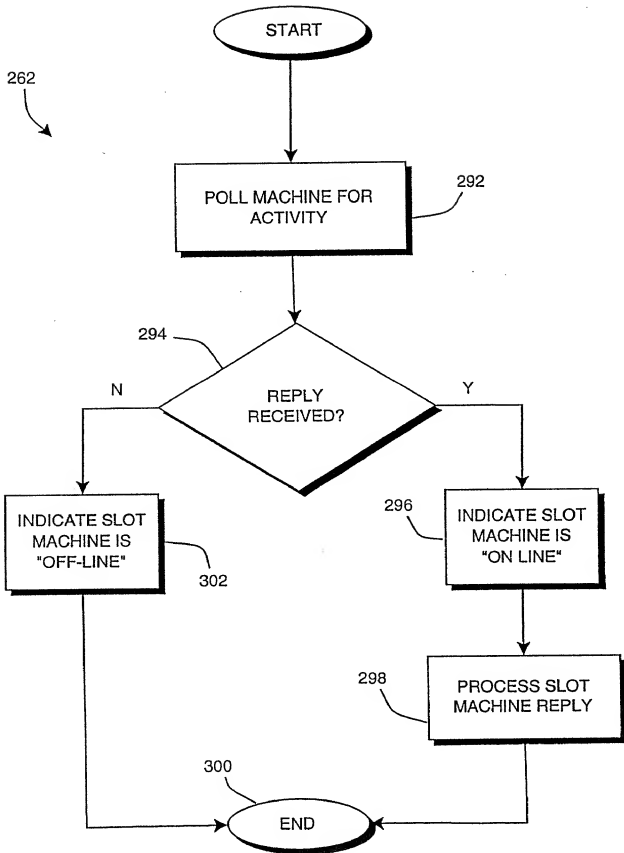


Fig.15

00000000000000000000



1050410-02622800

264

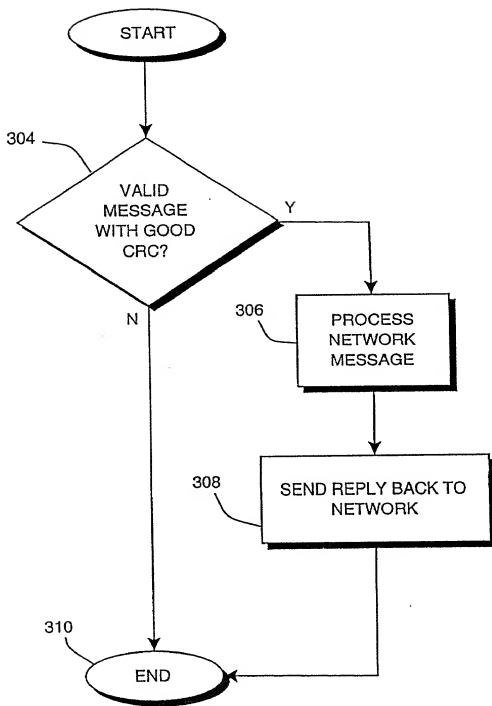


Fig.17

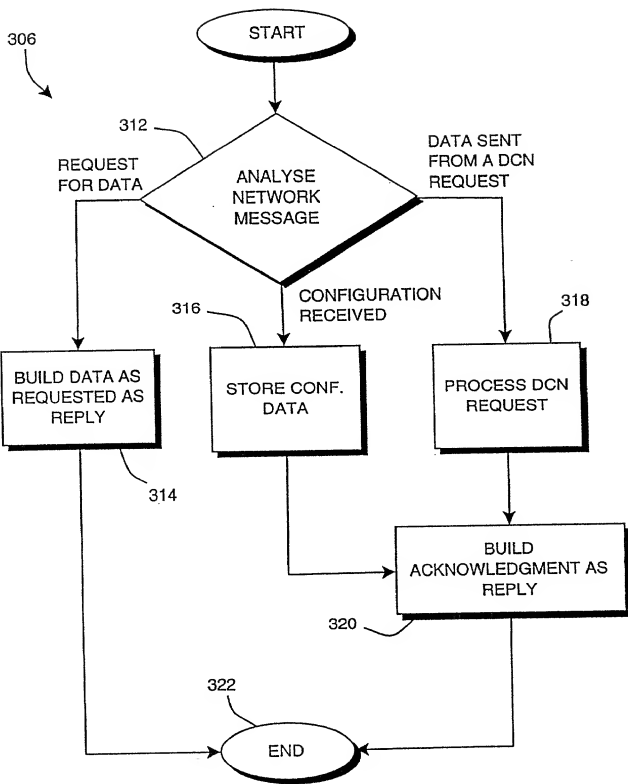


Fig.18

105040 0032800

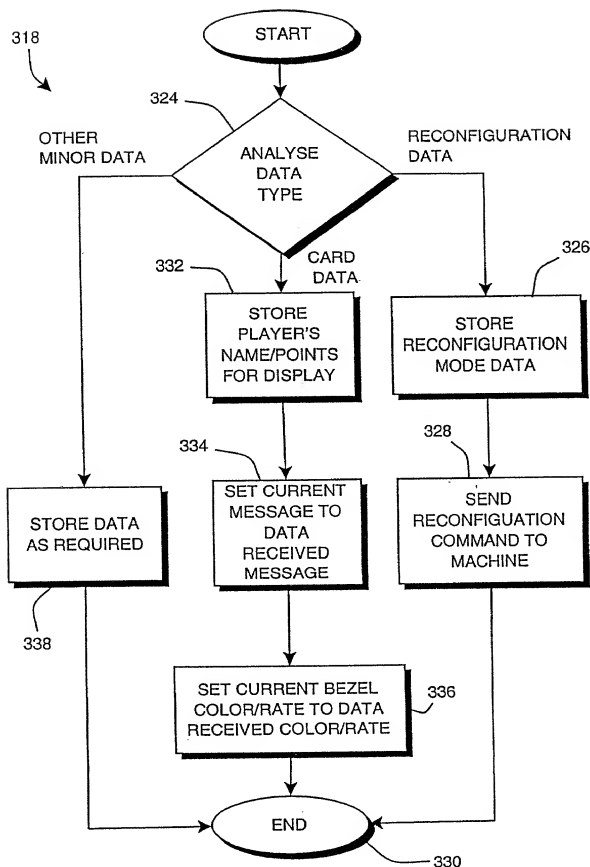


FIG. 10

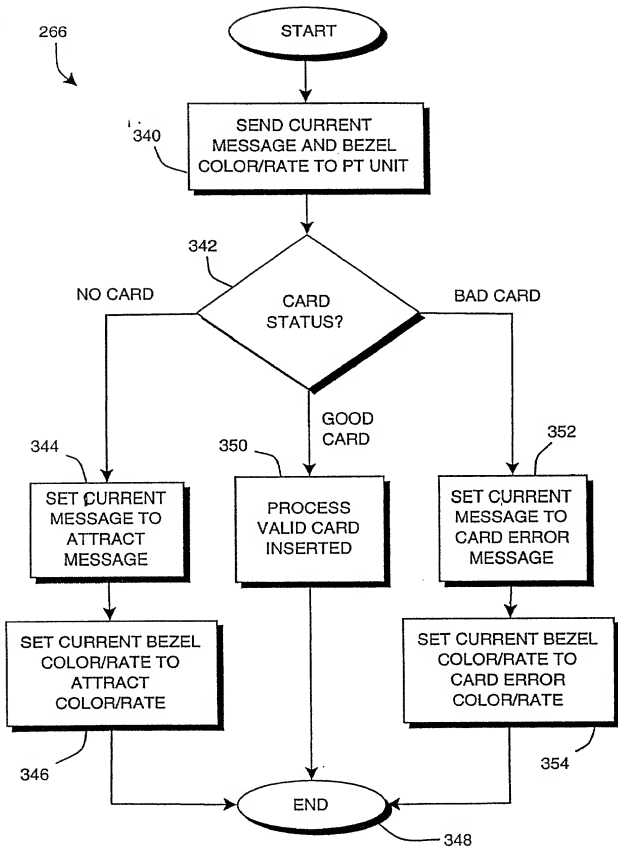


FIG 20

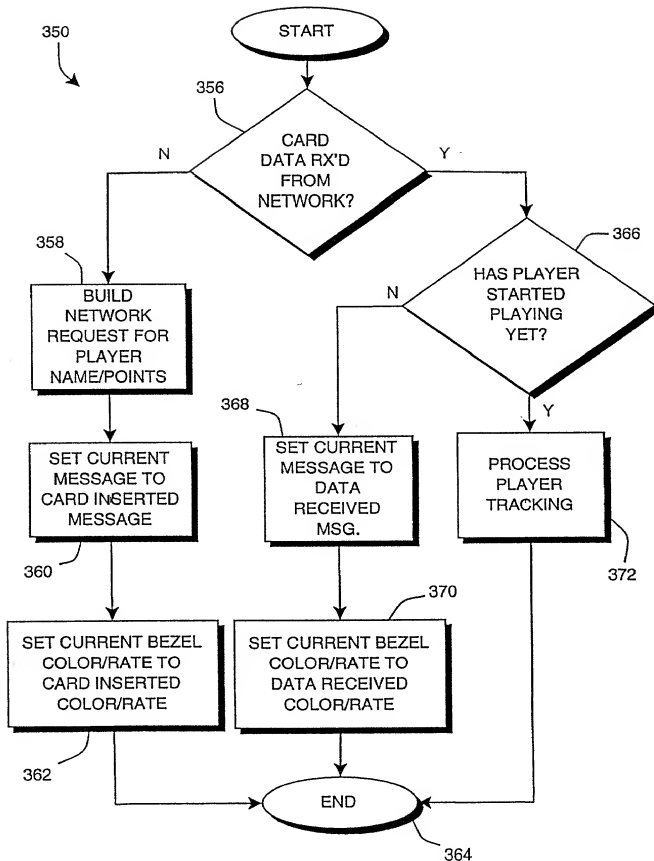


FIG 21

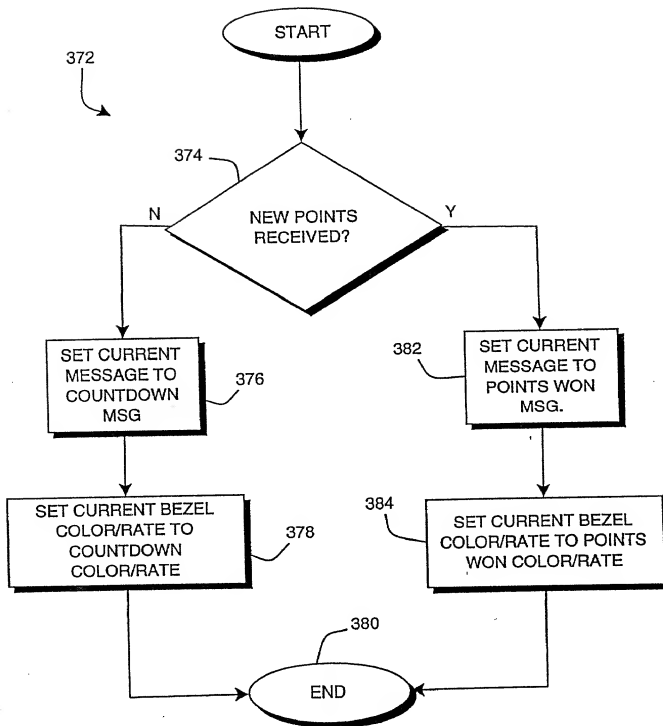
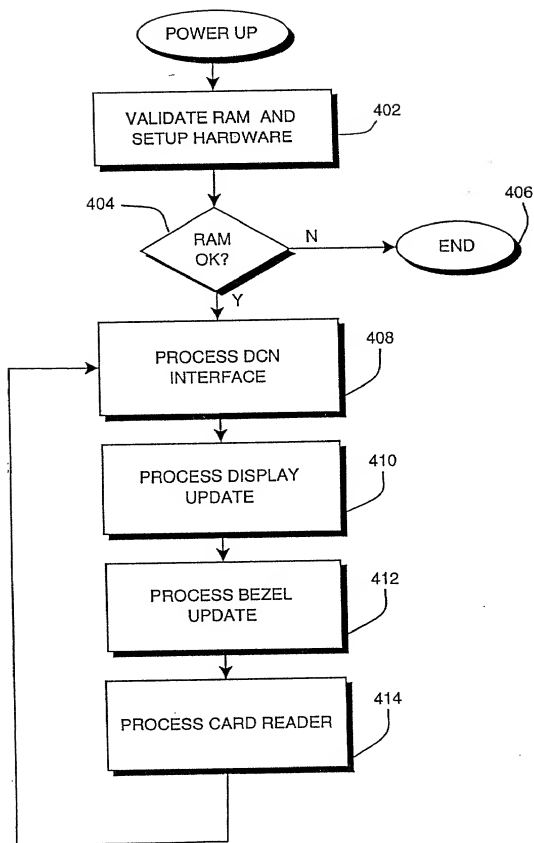


Fig.22

400



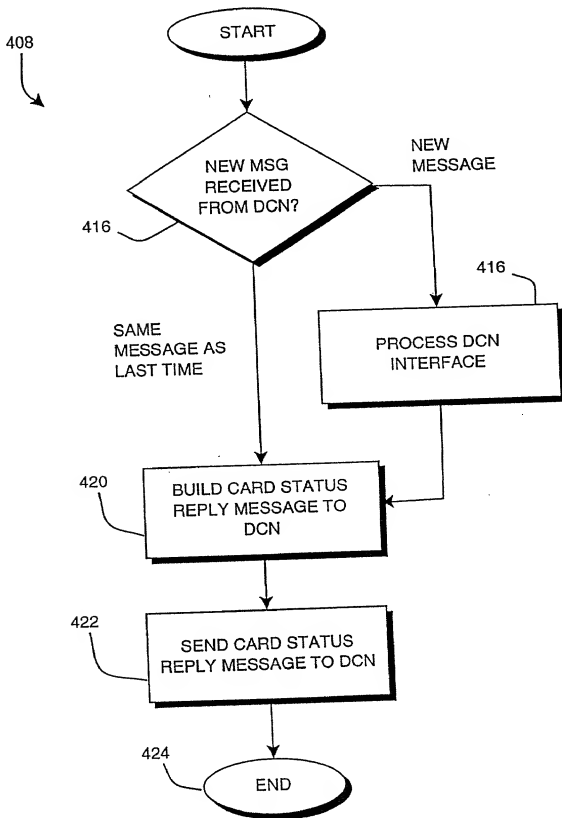
[illegible]

Fig.24

09927370-040601

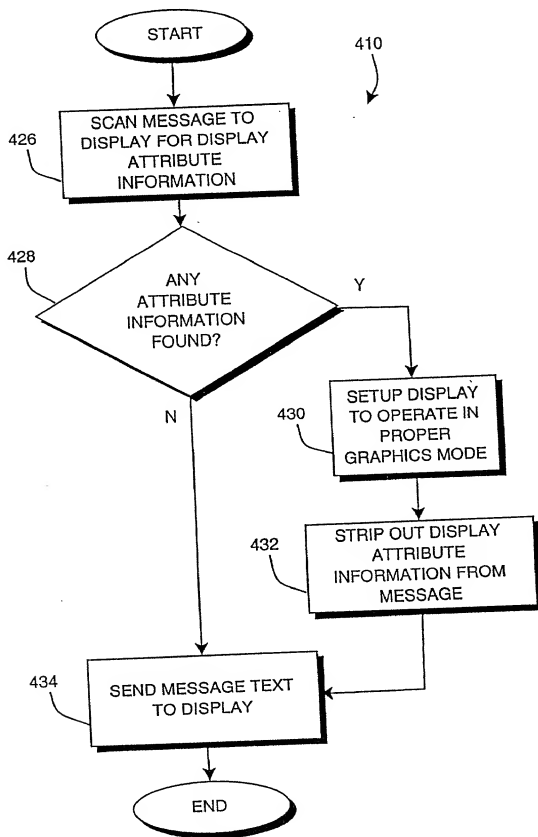


Fig.25

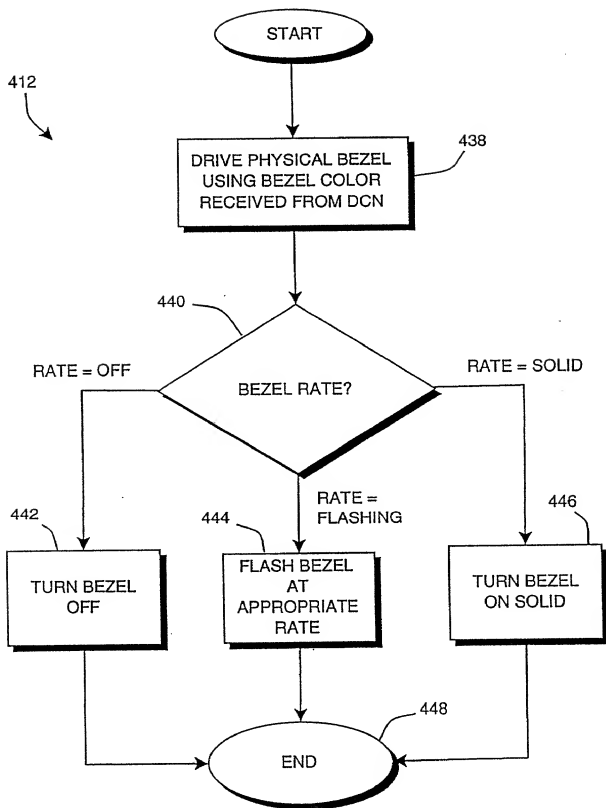


Fig.26

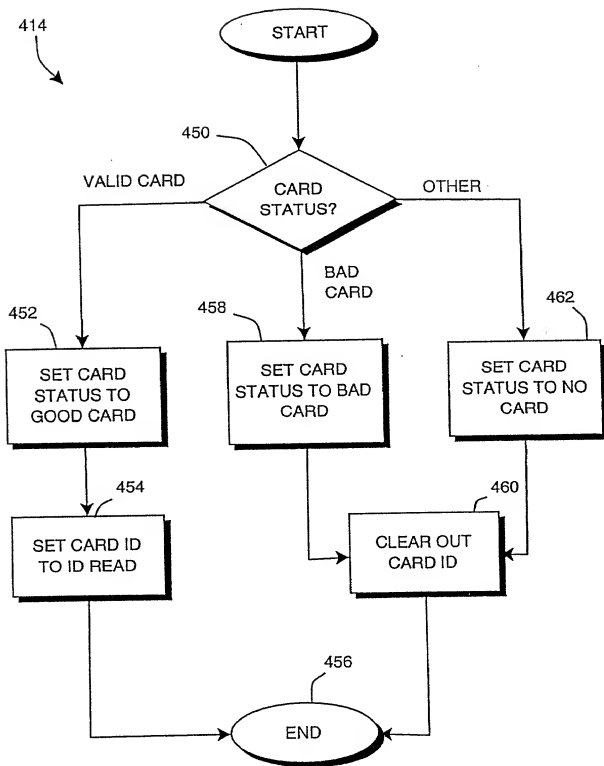
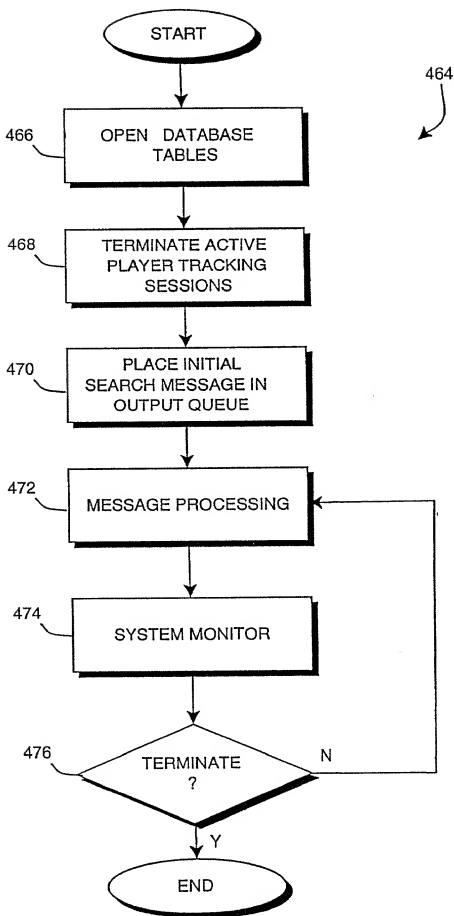


Fig.27

00827370 040601
108040 04822800



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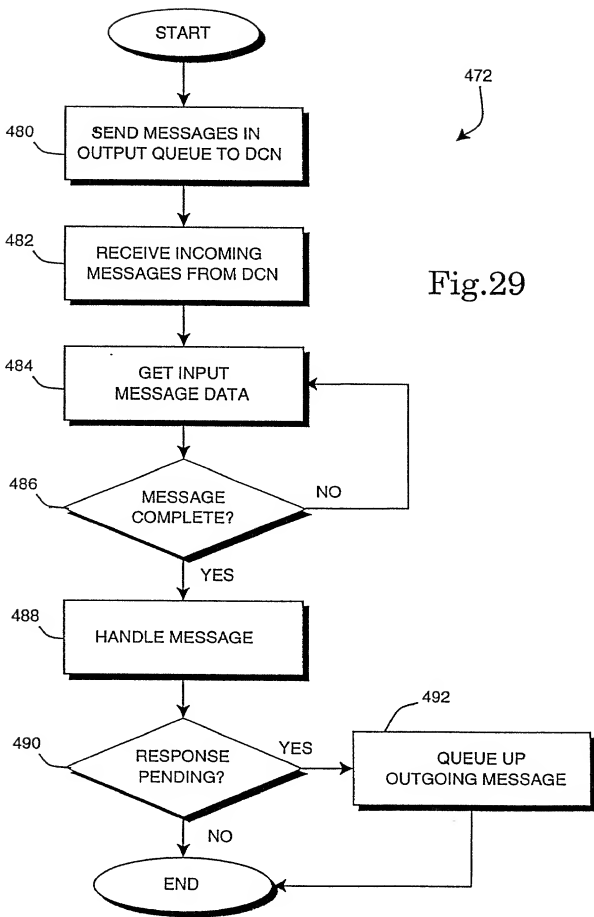
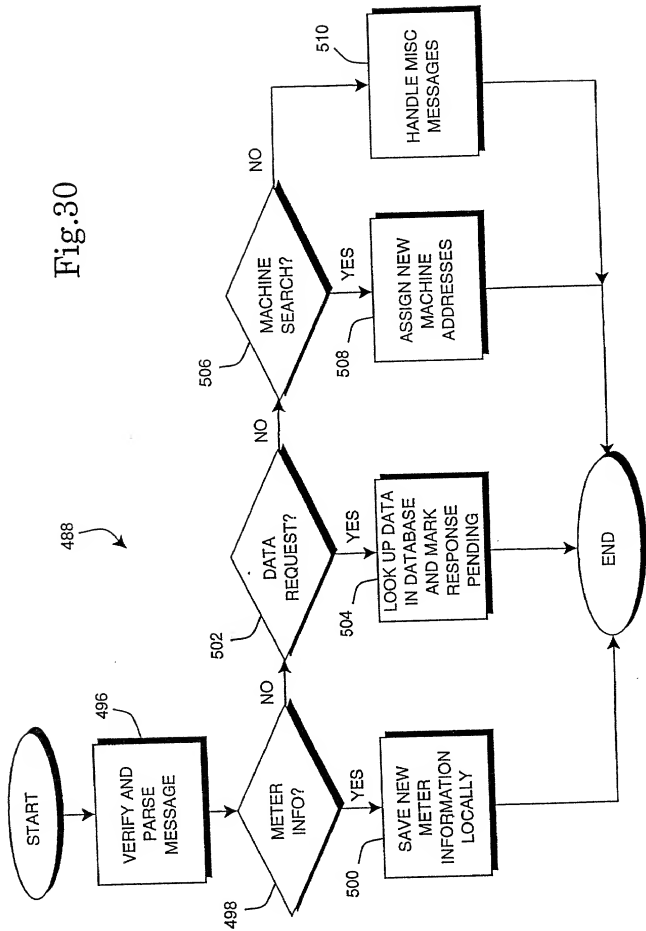
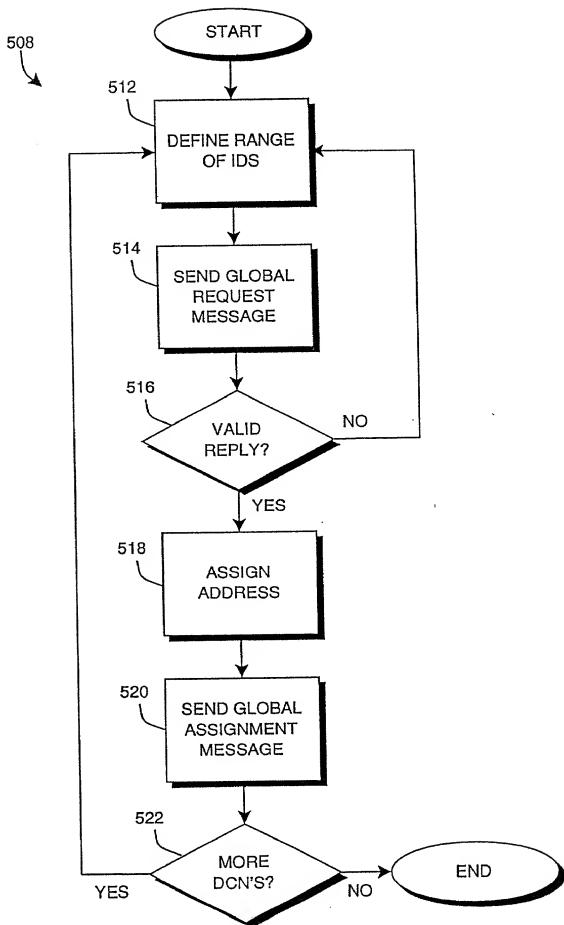


Fig.30





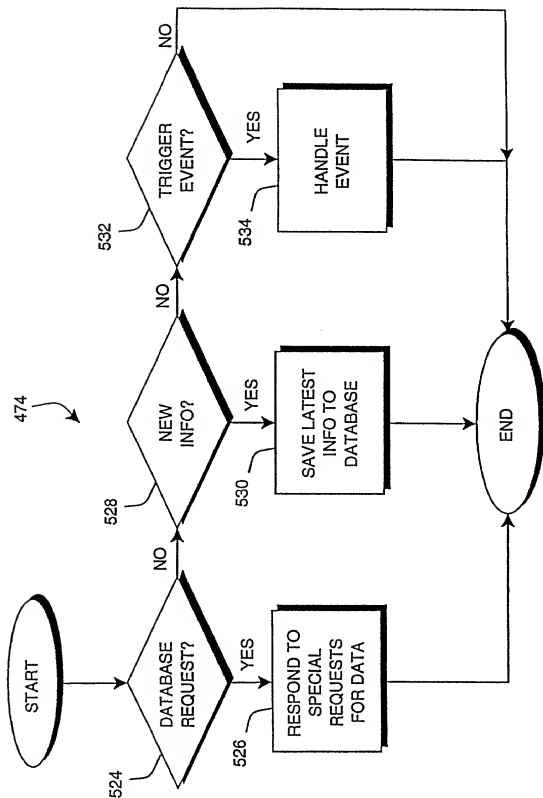


Fig.32

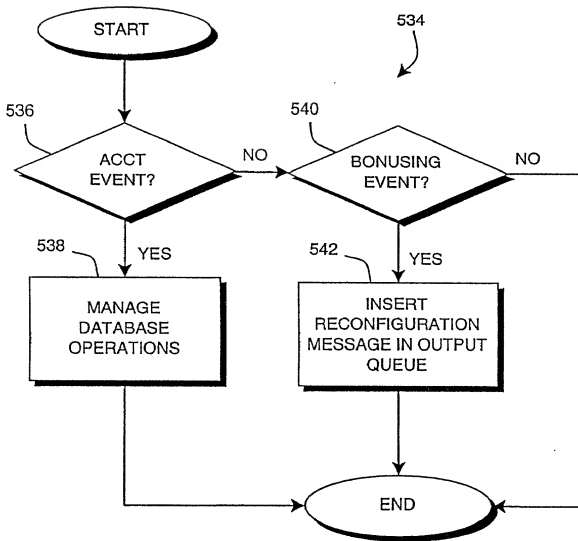


Fig.33

